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Application Number:	10570290	
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Title of invention:	Intergrated circuit and a method of cache remapping	
First Named Inventor/Applicant Name:	Adrianus Josephus Bink	
Customer Number:	65913	
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		NL031031_OAR_103009.pdf	141437	ves	13
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Multipart Description/PDF files in .zip description			
Document Description	Start	End	
Amendment/Req. Reconsideration-After Non-Final Reject	1	1	
Specification	2	2	
Claims	3	7	
Applicant Arguments/Remarks Made in an Amendment	8	13	

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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DD/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the filling Receipt. In due course.

New International Application Filed with the USPTO as a Receiving Office

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